

**Time:** 2 ½ Hours

**Total Marks:** 75

- N.B.:** (1) All Questions are Compulsory.  
(2) Make Suitable Assumptions Wherever Necessary And State The Assumptions Made.  
(3) Answer To The Same Question Must Be Written Together.  
(4) Number To The Right Indicates Marks.  
(5) Draw Neat Labeled Diagrams Wherever Necessary.  
(6) Use of Non – Programmable Calculator is allowed.

**Q.1 ATTEMPT ANY THREE QUESTIONS: (15 MARKS)**

- (A) Differentiate between Higher Level and Lower Level Programming Language. Also differentiate between Machine Language and Assembly Language. (5)  
(B) Draw and explain the block diagram of a computer with Microprocessor as CPU. (5)  
(C) Which are the basic operations performed by the microprocessor? Explain the bus structure of 8085 Microprocessor with necessary diagram. (5)  
(D) Illustrate the memory address range of a memory chip with 1K bytes of memory. Explain how range can be changed by modifying the hardware. (5)  
(E) With a neat diagram explain the functional pin configuration of the 40 pin IC8085. (5)  
(F) What is the use of memory segment 8155 in a SDK-85 System? Draw the block diagram of 8155. How is interfacing of 8155 Memory Section Done? (5)

**Q.2 ATTEMPT ANY THREE QUESTIONS: (15 MARKS)**

- (A) Explain the use of OUT instruction. Also explain how the instruction is executed with the help of Relevant Timing Diagram. (5)  
(B) Explain how 8 DIP switches can be interfaced with 8 bit 8085 microprocessor. Draw a neat diagram to show how data and address bus of the microprocessor can be used for interfacing. (5)  
(C) With a neat diagram discuss the programming model of 8085 microprocessor. (5)  
(D) Explain One Byte, Two Byte and Three Byte instructions. Give at least one example for each. (5)  
(E) List different addressing modes used by 8085 microprocessor. Write any one 1 byte and any one 2 byte instruction to perform arithmetic operation using 8085 microprocessor. (5)  
(F) Differentiate between conditional and unconditional jump used in 8085 microprocessor. Explain different conditional jump instructions. (5)

**Q.3 ATTEMPT ANY THREE QUESTIONS: (15 MARKS)**

- (A) A data FF H is stored at a memory location C050 H. Write three different ways of moving this data segment to Accumulator. (5)  
(B) Explain how 8085 microprocessor performs logical operation of comparing two data. (5)  
(C) What is Time Delay? Why is it needed? Explain how time delay can be generated using a register pair. (5)  
(D) Write an 8085 assembly language program to count continuously in hexadecimal from FF H to 00 H (in descending order) in a system with  $0.5\mu s$  clock period. Use register C to set up 1 ms delay between each count and display the number at one of the output ports. (5)  
(E) What is Stack? Explain the use of SP register in 8085. Discuss the instructions PUSH and POP. Write a code to demonstrate use of any one instruction. (5)  
(F) Explain the following concepts for subroutine program:  
(i) Nesting  
(ii) Multiple Ending Subroutine (5)

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**Q.4 ATTEMPT ANY THREE QUESTIONS: (15 MARKS)**

- (A) Write steps to convert a binary number to BCD. Write a program to convert given 8 bit binary number to BCD. (5)  
(B) Explain the use of DAA instruction. Also, perform the following operations with given packed BCD numbers: (5)  
    (i)  $77 + 48$   
    (ii)  $84 - 48$   
(C) What are utility programs? What is their use in software development systems? Discuss various tools used for developing software assembly language programs. (5)  
(D) Write a note on two pass assembler. Support your explanation with a program, if necessary. (5)  
(E) What do you mean by vectored interrupts? Discuss each of 8085 vectored interrupt in brief. (5)  
(F) Write a note on DMA. (5)

**Q.5 ATTEMPT ANY THREE QUESTIONS: (15 MARKS)**

- (A) What are the different types of special Pentium Register? Describe them in brief. (5)  
(B) With a neat Internal Block Diagram. Explain the Internal Structure of Pentium Pro Microprocessor. (5)  
(C) Discuss the SYSENTER and SYSEXIT instructions of Pentium II Processor. (5)  
(D) Explain Hyper Threading Technology. Draw a neat diagram to indicate how Dual Core Processor is constructed. (5)  
(E) Discuss the feature of SPARC Microprocessor. (5)  
(F) What are the basic categories of SPARC instructions? Discuss any two categories. (5)
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